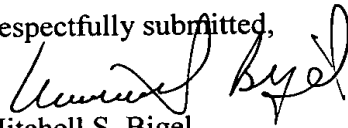


In re: Paul D. Franzon, Stephen E. Mick, John M. Wilson
Serial No.: To Be Assigned
Filed: Concurrently Herewith
Page 2 of 3

REMARKS

This amendment is being filed to correct a typographical error in the specification. Entry of this amendment and examination and allowance of the present application is respectfully requested.

Respectfully submitted,


Mitchell S. Bigel
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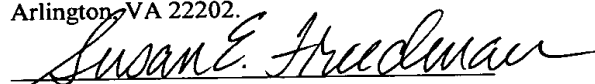
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Susan E. Freedman

Date of Signature: November 29, 2001

VERSION WITH MARKINGS TO SHOW CHANGES MADE

The following is an addendum to the concurrently filed Preliminary Amendment in the above-referenced application. This addendum includes a marked-up version of the changes made to the specification by the present Preliminary Amendment.

In the Specification:

The paragraph at Page 14, line 22-Page 15, line 3 has been amended as follows:

Finally, due to a lack of a DC path, conventional driver and receiver circuits may not be preferred for embodiments of the present invention. In [other] order to at least partially compensate for a lack of a DC path, it may be desirable to use Non-Return-to-Zero (NRZ) tolerant and/or other DC offset compensating receivers that can, for example, use feedback, so as to reduce or prevent the average DC shift that may occur over time when a long string of zeros or ones are sent. Such circuits have been used in optical transceivers, and may also be used with AC-coupled structures according to embodiments of the present invention. Moreover, as was described above, current mode signaling may be used which can provide advantages over voltage mode signaling. Current mode signaling can be faster as the input impedance of the receiver can be small. Also, it can be low noise, because there can be reduced di/dt and/or return path noise.